

Attorney's Docket No.: Intel 10559/325001/P9686

Amendment to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

1. (Cancelled)

2. (Currently Amended) ~~The method of claim 1~~ A method comprising:

receiving, from an agent, a request to read data from a read address in pre-fetchable data storage;

retrieving an initial amount of data determined by a pre-fetch factor;

terminating the retrieving;

determining if the requesting agent received as much data as requested when the retrieving is terminated; and

if the requesting agent did not receive as much data as requested, storing a next read address at which data would have been retrieved, had the retrieving not been terminated

~~further comprising,~~ before said retrieving, comparing the read address to a stored next read address, and if the read address and the stored next read address match, retrieving an amount of data determined by both the pre-fetch factor and a re-read pre-fetch factor.

Attorney's Docket No.: Intel 10559/325001/P9686

3. (Currently Amended) ~~The method of claim 1~~ A method  
comprising:

receiving, from an agent, a request to read data from a  
read address in pre-fetchable data storage;

retrieving an initial amount of data determined by a pre-  
fetch factor;

terminating the retrieving;

determining if the requesting agent received as much data  
as requested when the retrieving is terminated; and

if the requesting agent did not receive as much data as  
requested, storing a next read address at which data would have  
been retrieved, had the retrieving not been terminated

~~further comprising~~ changing a the re-read pre-fetch factor  
based upon the determining.

4. (Currently Amended) The method of claim ~~1~~ 2, wherein said  
determining includes determining if the retrieving terminated  
early.

5. (Previously Presented) The method of claim 3 further  
comprising changing the re-read pre-fetch factor after a time  
interval.

6. (Currently Amended) The method of claim ~~1~~ 2 wherein the  
value of the pre-fetch factor is alterable.

Attorney's Docket No.: Intel 10559/325001/P9686

7. (Previously Presented) The method of claim 3 wherein changing the re-read pre-fetch factor comprises selectively enabling and disabling incrementing the re-read pre-fetch factor.

8. (Previously Presented) A system comprising:

a computer having at least one agent, at least one bridge, a pre-fetch factor register, a re-read pre-fetch factor register and a next read address register;

the bridge being configured to:

(a) receive from an agent a request to read data from a read address in pre-fetchable data storage;

(b) read an amount of data determined by a value stored in the pre-fetch factor register;

(c) determine if the requesting agent has received the full amount of requested data when the read is terminated; and

(d) if the requesting agent did not receive the full amount of requested data, increment a value in the re-read pre-fetch factor register.

9. (Previously Presented) The system of claim 8 wherein the bridge is further configured, if the requesting agent did not receive the full amount of requested data, to store a next read address in the next read address register.

Attorney's Docket No.: Intel 10559/325001/P9686

10. (Currently Amended) The system of claim 9, the bridge being further configured to compare the read address to the stored next read address, and if the read address and the stored next read address match, increasing the bridge being further configured to increase the amount of data requested by a value in the re-read pre-fetch factor register.

11. (Previously Presented) The system of claim 8, the bridge being further configured to change the value in the re-read pre-fetch factor register based upon the determination.

12. (Previously Presented) The system of claim 8, the bridge being further configured to decrement the pre-fetch factor register after a time interval.

13. (Previously Presented) The system of claim 8 wherein the contents of the pre-fetch factor register are alterable.

14. (Previously Presented) The system of claim 8 wherein the bridge is further configured to enable and disable the application of the pre-fetch register and the re-read pre-fetch register under control of the computer.

Attorney's Docket No.: Intel 10559/325001/P9686

15. (Original) The system of claim 8 wherein the pre-fetch register is contained within the bridge.

16. (Original) The system of claim 8 wherein the re-read pre-fetch register is contained within the bridge.

17. (Cancelled)

18. (Currently Amended) ~~The computer program product of claim 17~~ A computer program product, disposed on a computer readable medium, comprising instructions to cause a computer to:

receive from an agent a request to read data from a read address in pre-fetchable data storage;

read an amount of data determined by a value stored in a pre-fetch factor register;

determine if the requesting agent has received the full amount of requested data when the read terminates;

if the requesting agent did not receive the full amount of requested data, store a next read address at which data would have been retrieved had the retrieving not been terminated; and  
~~further comprising instructions to cause the computer to compare the read address to the stored next read address, and if the read address and the stored next read address match, request an amount of data determined by a value in the a re-read pre-fetch factor register.~~

Attorney's Docket No.: Intel 10559/325001/P9686

19. (Currently Amended) ~~The computer program product of claim~~

~~17~~ A computer program product, disposed on a computer readable medium, comprising instructions to cause a computer to:

receive from an agent a request to read data from a read address in pre-fetchable data storage;

read an amount of data determined by a value stored in a pre-fetch factor register;

determine if the requesting agent has received the full amount of requested data when the read terminates;

if the requesting agent did not receive the full amount of requested data, store a next read address at which data would have been retrieved had the retrieving not been terminated; and  
~~further comprising instructions~~ causing the computer to increment the a re-read pre-fetch factor register based upon the determining.

20. (Currently Amended) The computer program product of claim ~~17~~ 18 further comprising instructions causing the computer to decrement the pre-fetch factor register after a time interval.

21. (Currently Amended) ~~The computer program product of claim~~  
~~17~~ A computer program product, disposed on a computer readable medium, comprising instructions to cause a computer to:

Attorney's Docket No.: Intel 10559/325001/P9686

receive from an agent a request to read data from a read address in pre-fetchable data storage;

read an amount of data determined by a value stored in a pre-fetch factor register;

determine if the requesting agent has received the full amount of requested data when the read terminates;

if the requesting agent did not receive the full amount of requested data, store a next read address at which data would have been retrieved had the retrieving not been terminated; and  
wherein the pre-fetch factor register, the a re-read pre-fetch register and the next read address are contained within a bridge.

22. (Currently Amended) The computer program product of claim ~~17~~ 18 wherein the instructions are stored in and implemented by a bridge.